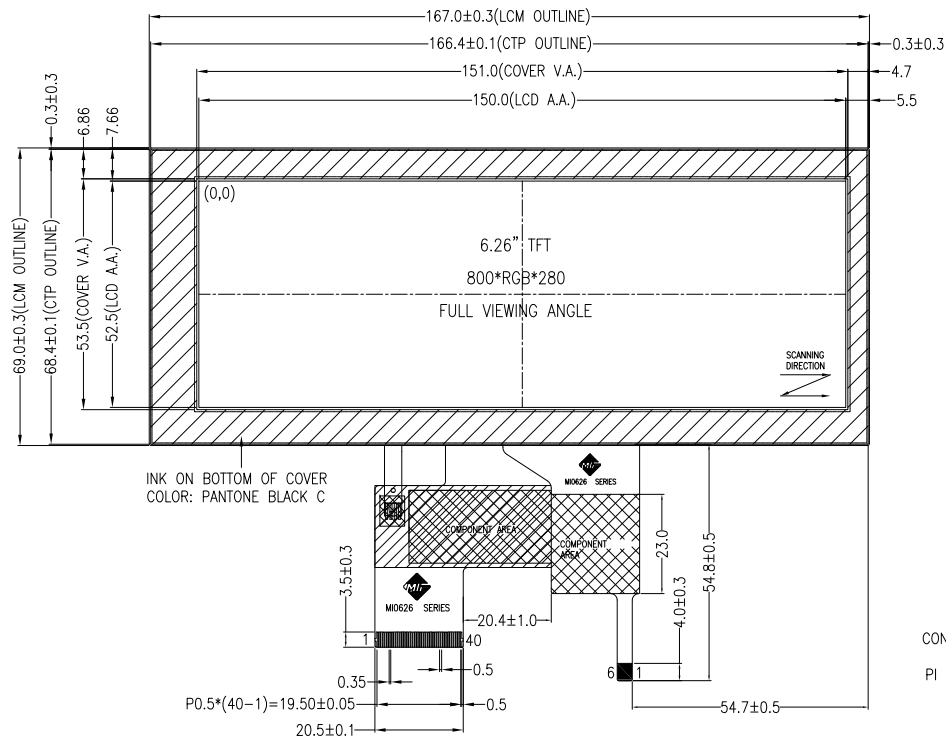


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL.



NOTES FOR TFT:

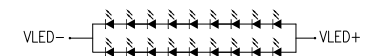
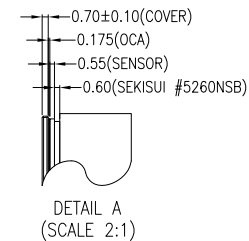
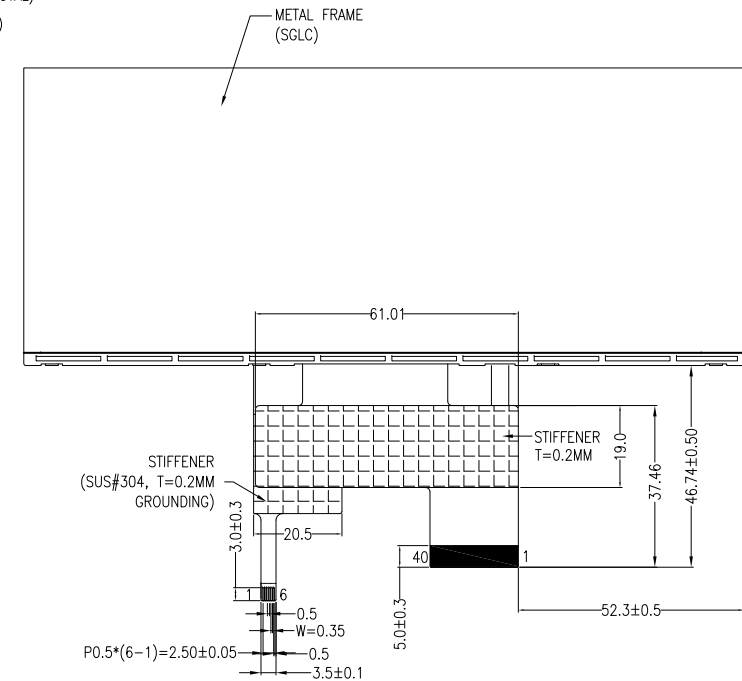
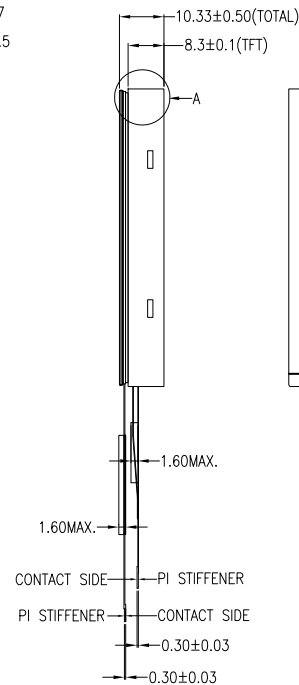
1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=3.3V
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. LCD DRIVER: HX8272
5. LED BACKLIGHT: 18-CHIP WHITE LED, If=40mA, Vf=27V±1.8V
6. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

OTHER NOTES:

1. OPERATING TEMP: -20°C ~ 70°C
2. STORAGE TEMP: -30°C ~ 80°C
3. SURFACE LUMINANCE: 480 CD/M<sup>2</sup> (TYP.) WITH CTP
4. ONLY THE SINGLE LAYER AREA ON FPC IS BENDABLE AND THE MINIMUM BEND RADIUS IS 1.5MM
5. GENERAL TOLERANCE: ±0.3MM
6. ROHS COMPLIANT

NOTES FOR CTP:

1. 6.26" PROJECTIVE CAPACITIVE TOUCH PANEL
2. OPERATING VOLTAGE: VCC=3.3V
3. CHEMICAL STRENGTHENED COVER GLASS + SENSOR GLASS + FPCA
4. IC DRIVER: ILI2511(SMT), IIC INTERFACE
5. MULTI-FINGER: UP TO 5
6. HARDNESS OF COVER SURFACE: 7H(750g)



### BACKLIGHT CIRCUIT DIAGRAM

| PIN ASSIGNMENT FOR CTP |        | PIN ASSIGNMENT FOR LCM |             |
|------------------------|--------|------------------------|-------------|
| PIN                    | SYMBOL | PIN                    | SYMBOL      |
| 1                      | VSS    | 1                      | VLED-       |
| 2                      | SDA    | 2                      | VLED+       |
| 3                      | SCL    | 3                      | GND         |
| 4                      | VCC    | 4                      | VDD         |
| 5                      | INT    | 5                      | R0          |
| 6                      | RESET  | 6                      | R1          |
|                        |        | 7                      | R2          |
|                        |        | 8                      | R3          |
|                        |        | 9                      | R4          |
|                        |        | 10                     | R5          |
|                        |        | 11                     | R6          |
|                        |        | 12                     | R7          |
|                        |        | 13                     | G0          |
|                        |        | 14                     | G1          |
|                        |        | 15                     | C2          |
|                        |        | 16                     | C3          |
|                        |        | 17                     | G4          |
|                        |        | 18                     | O5          |
|                        |        | 19                     | C6          |
|                        |        | 20                     | G7          |
|                        |        | 21                     | B0          |
|                        |        | 22                     | B1          |
|                        |        | 23                     | B2          |
|                        |        | 24                     | B3          |
|                        |        | 25                     | B4          |
|                        |        | 26                     | B5          |
|                        |        | 27                     | B6          |
|                        |        | 28                     | B7          |
|                        |        | 29                     | GND         |
|                        |        | 30                     | CLK         |
|                        |        | 31                     | DISP        |
|                        |        | 32                     | HSYNC       |
|                        |        | 33                     | VSNC        |
|                        |        | 34                     | DE          |
|                        |        | 35                     | NC(ATREN)   |
|                        |        | 36                     | GND         |
|                        |        | 37                     | NC(XRES)    |
|                        |        | 38                     | NC(VDD_OTP) |
|                        |        | 39                     | NC(I2C_SCL) |
|                        |        | 40                     | NC(I2C_SDA) |



MULTI-INNO TECHNOLOGY CO., LTD.

|              |      |            |                                                                                       |                                  |
|--------------|------|------------|---------------------------------------------------------------------------------------|----------------------------------|
| DRAWN BY:    | JACK | 2023.12.21 |  | MODULE P/N:<br>MI0626AT-2CP      |
| CHECKED BY:  |      |            | SCALE : 1/1                                                                           |                                  |
| APPROVED BY: |      |            | UNIT: MM                                                                              | DESCRIPTION:<br>TFT + CTP MODULE |
| VERSION NO:  | 01   |            | SHEET NO.: 1/1                                                                        |                                  |

|      |                     |         |            |
|------|---------------------|---------|------------|
| 01   | FIRST ISSUE         | JACK    | 2023.12.21 |
| VER. | REVISED DESCRIPTION | REVISER | DATE       |